L Number	Hits	Search Text	DB	Time stamp
_	609	"delay circuit" with clock same "data signal"	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/05/22 14:00
_	1194	(365/194).CCLS.	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/05/22 14:01
-	2202	(365/189.05).CCLS.	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/05/22 14:01
-	2353	(365/233).CCLS.	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/05/22
-	7150	(delay or delayed) with data with buffer	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/05/22 14:02
-	67	("delay circuit" with clock same "data signal") same buffer	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/05/22 14:07
-	83	input near3 data near3 buffer same clock same delay same "delay circuit"	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/05/22 14:38
-	127	input near3 data near3 buffer same (delay delayed delaying) near3 clock	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/05/22
-	1	"delay information" same "delay circuit" same clock same buffer	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/05/22
_	2162	"delay information"	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/05/22 14:41
-	12	"delay buffer" same clock same data same (selective selecting select)	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/05/24
-	382	(programmable controllable) with delay with buffer	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/05/24
-	1429	(programmable controllable variable) adj3 delay same buffer	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2003/05/24 10:56
-	1222	(programmable controllable variable) adj delay same buffer	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/24 10:58

Search History 5/28/03 1:12:57 PM Page 1

-	302	((programmable controllable variable) adj	USPAT;	2003/05/24
		delay same buffer) same clock near3	US-PGPUB;	13:46
		(delay delaying delayed)	EPO; JPO;	1 .4
			DERWENT;	1
			IBM_TDB	2222425
-	6		USPAT	2003/05/24
j		"5608896" "5812472" "5815016").PN.		13:47
-	2		USPAT;	2003/05/27
		clock same PLL same delay	US-PGPUB;	09:36
			EPO; JPO;	
			DERWENT;	
	l		IBM_TDB	
-	23	1	USPAT;	2003/05/27
		same PLL same delay	US-PGPUB;	09:39
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	2222/25/25
_	83	"memory module" same "delay circuit"	USPAT;	2003/05/27
			US-PGPUB;	10:32
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	0000/05/05
-	187	1 -	USPAT;	2003/05/27
		delay	US-PGPUB;	11:09
			EPO; JPO;	
			DERWENT;	
		,, , ,	IBM_TDB	0000/05/07
-	1511	yoshimori.in.	USPAT;	2003/05/27
			US-PGPUB;	11:09
			EPO; JPO;	
			DERWENT;	
	3.5		IBM_TDB	2002 (05 /27
_	35	yoshimori.in. and integrated.ti.	USPAT;	2003/05/27
			US-PGPUB;	11:44
			EPO; JPO;	
			DERWENT;	
	744	DLL with PLL	IBM_TDB USPAT;	2003/05/27
_	/44	DDD with bur	US-PGPUB;	11:44
			EPO; JPO;	11.44
			DERWENT;	
			IBM TDB	
_	478	DLL near3 PLL	USPAT;	2003/05/27
	1,0	222	US-PGPUB;	11:44
			EPO; JPO;	1
			DERWENT;	•
			IBM TDB	
_	454	(DLL near3 PLL) and clock	USPAT;	2003/05/27
		•	US-PGPUB;	11:45
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
-	352	(DLL near3 PLL) same clock	USPAT;	2003/05/27
			US-PGPUB;	11:45
			EPO; JPO;	
			DERWENT;	
]			IBM_TDB	
-	210	(DLL near3 PLL) same clock same delay	USPAT;	2003/05/27
1		-	US-PGPUB;	14:46
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	216	DLL.ti.	USPAT;	2003/05/27
]			US-PGPUB;	11:52
			EPO; JPO;	
]			DERWENT;	
			IBM_TDB	0000/07/07
-	709	(delay and locked and loop).ti.	USPAT;	2003/05/27
			US-PGPUB;	11:56
1 1			EPO; JPO;	ŀ
			DERWENT; IBM TDB	

Search History 5/28/03 1:12:57 PM Page 2

-	157	(delay and locked and loop).ti.	USPAT	2003/05/27
-	66	(delay and locked and loop).ti. and PLL	USPAT	13:39 2003/05/27 13:40
-	5	"delayed" near3 "phase-lock"	USPAT; US-PGPUB;	2003/05/27
			EPO; JPO;	
			DERWENT;	